

# Clustering for Processing Rate Optimization

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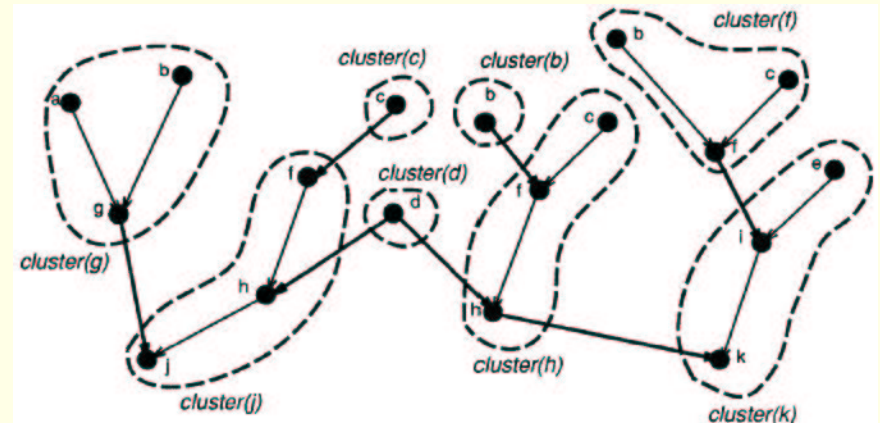
November 7, 2005

# Outline

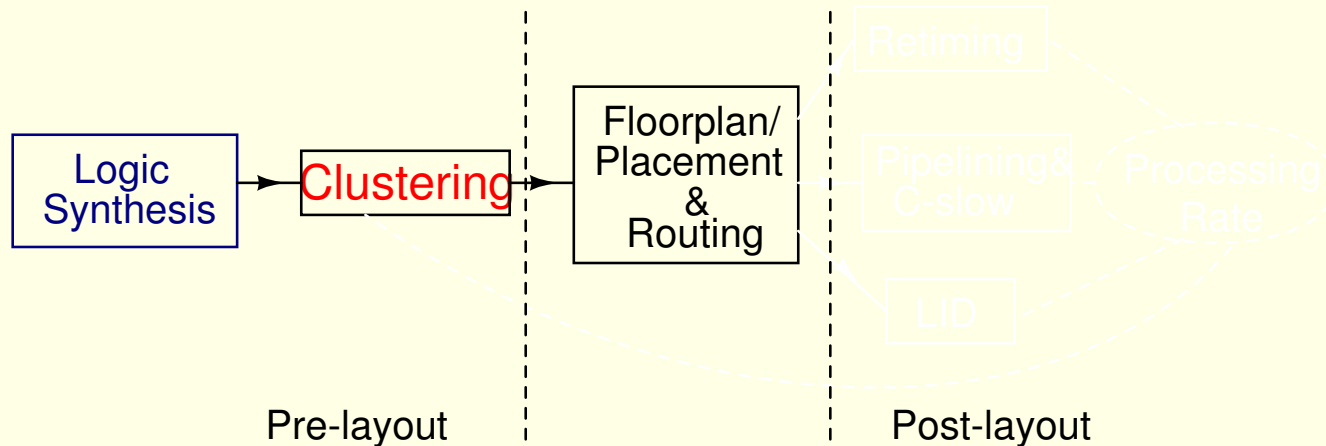
- Motivation
- Problem formulation
- Algorithm
- Experimental results
- Conclusions

# A logical and physical design flow

- **Clustering** decomposes a circuit into clusters

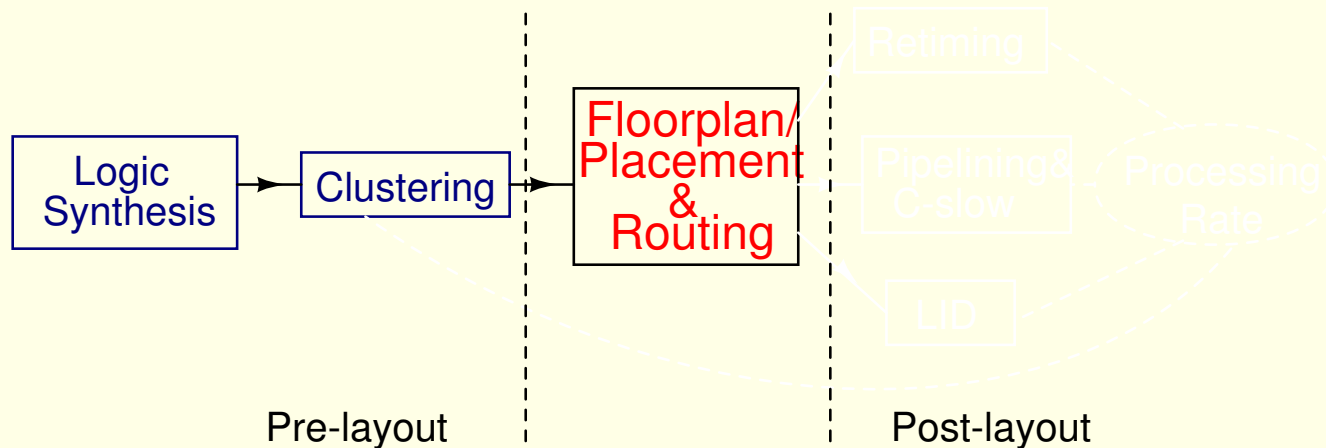


Rajaraman and Wong [DAC'93]



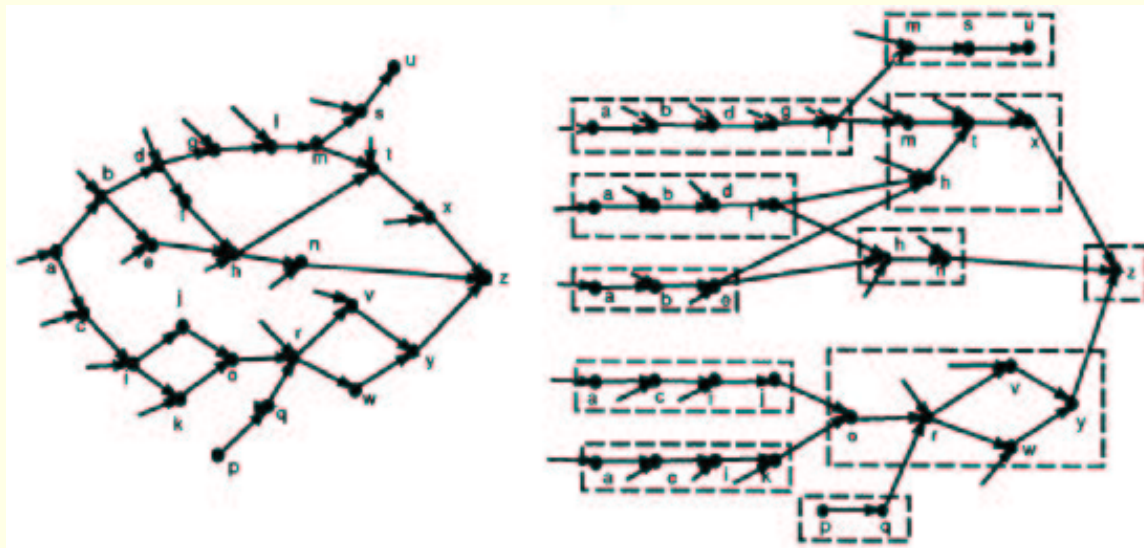
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- Floorplan/placement & routing generate a layout



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  - Inter-cluster interconnects may become global (**long**)



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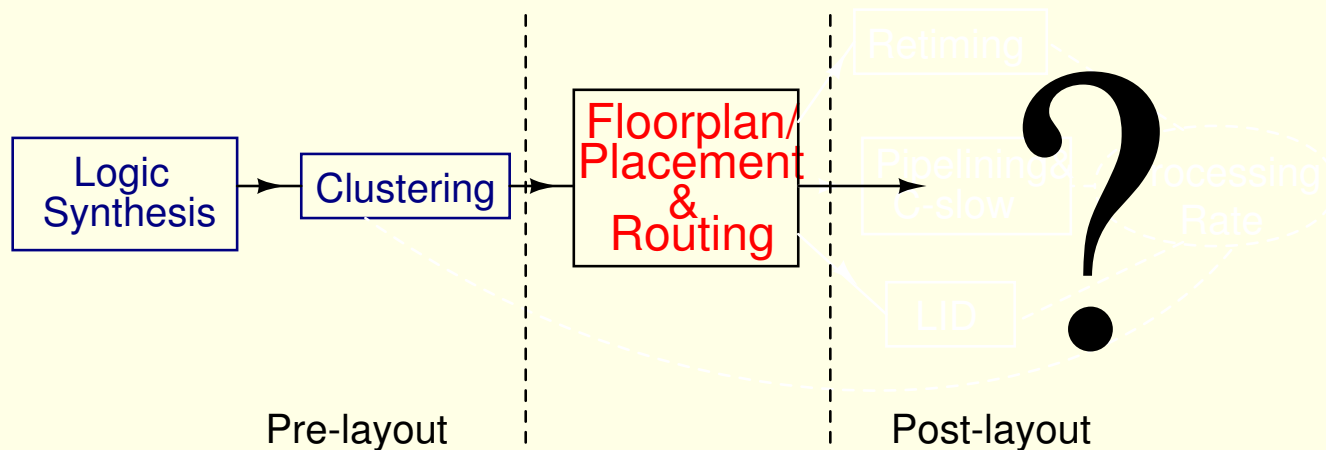
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    - \* Delay NOT available at logic synthesis but after layout

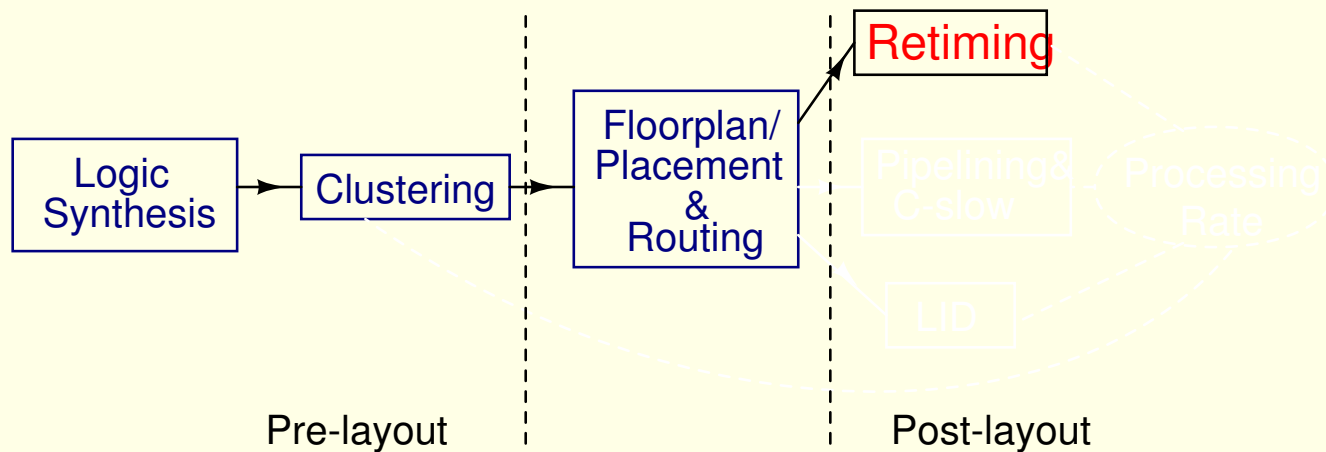
## A logical and physical design flow

- **Floorplan/placement & routing** generate a layout
  - Intra-cluster interconnects are local (**short**)
  - Inter-cluster interconnects may become global (**long**)
- Correct design at logic synthesis may **FAIL** to meet timing



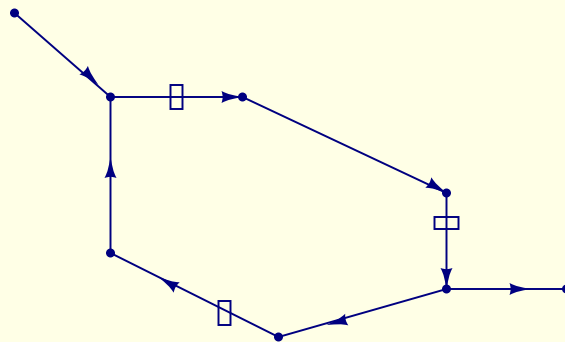
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  - Period lower bounded



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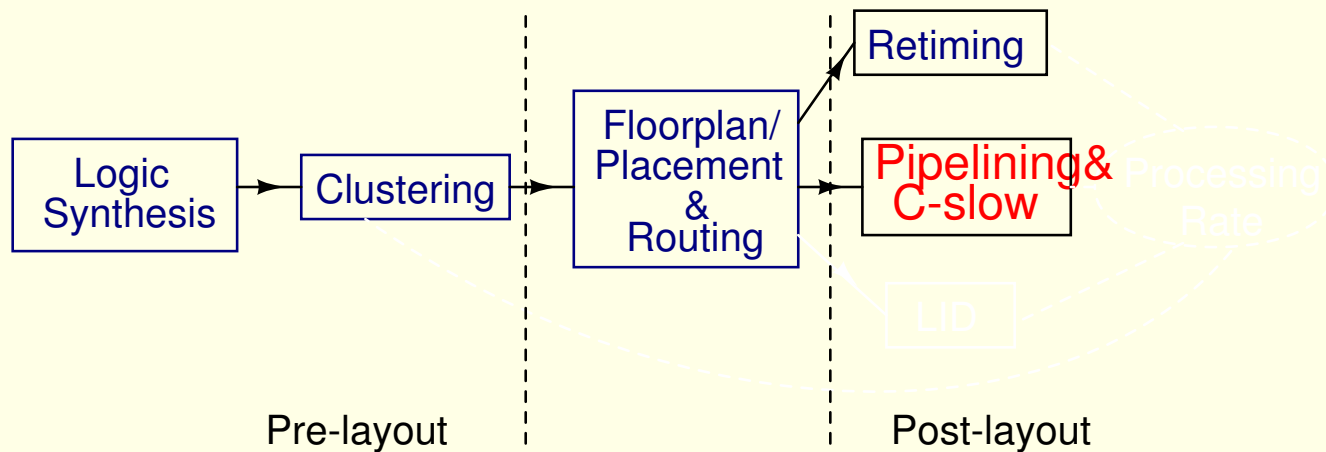
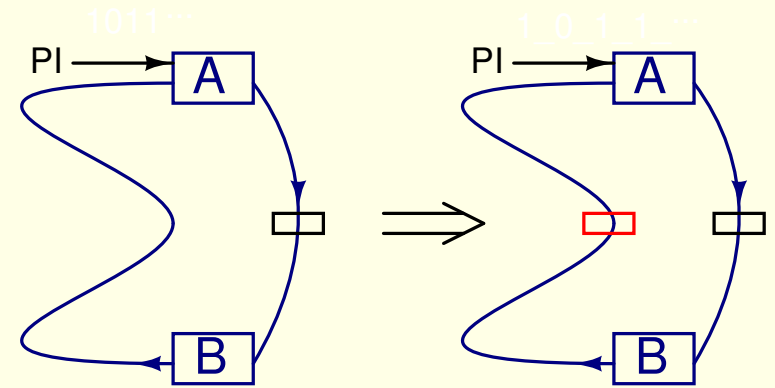
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- **Pipelining** inserts FFs in a cycle

- Function changed [Nookala and Sapatrenkar DAC'04]

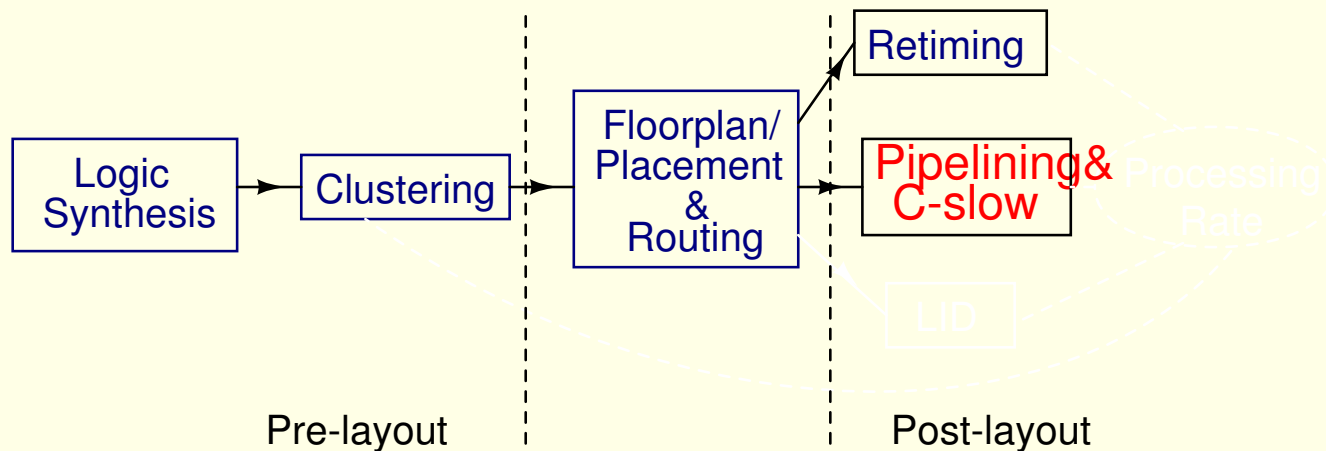
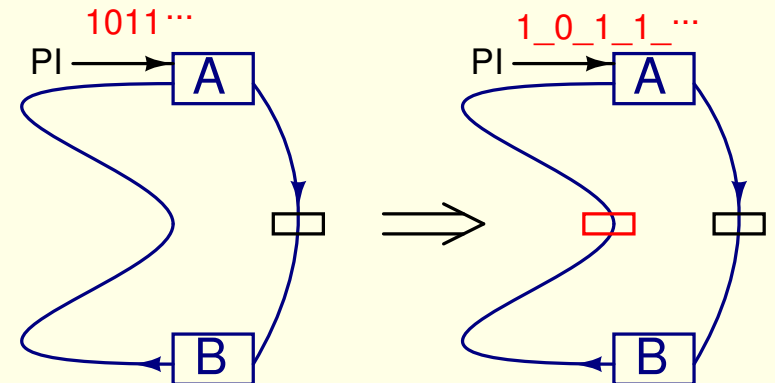
- *C*-slow transformation

- Slows down input issue rate, i.e., throughput becomes  $1/C$



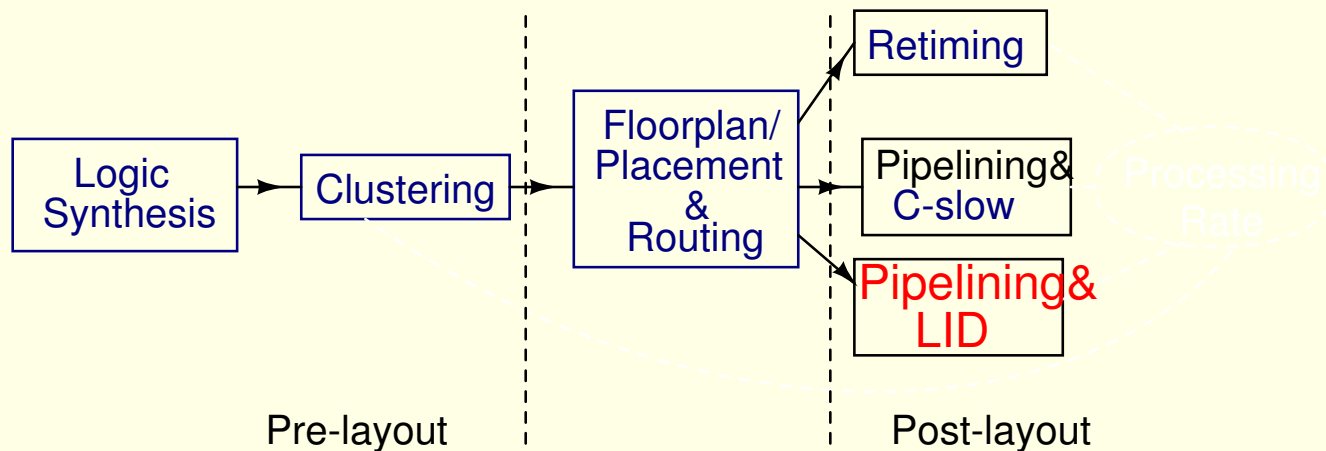
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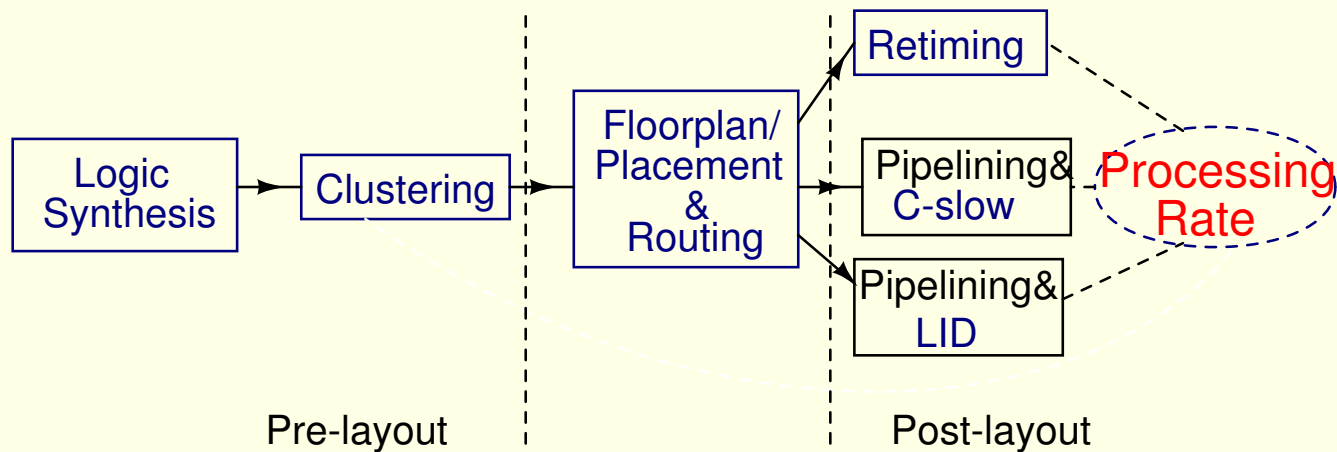
## A logical and physical design flow

- **Latency insensitive design (LID)** [Carloni ICCAD'99]
  - Employ a protocol that slows down a part of the circuit only when necessary



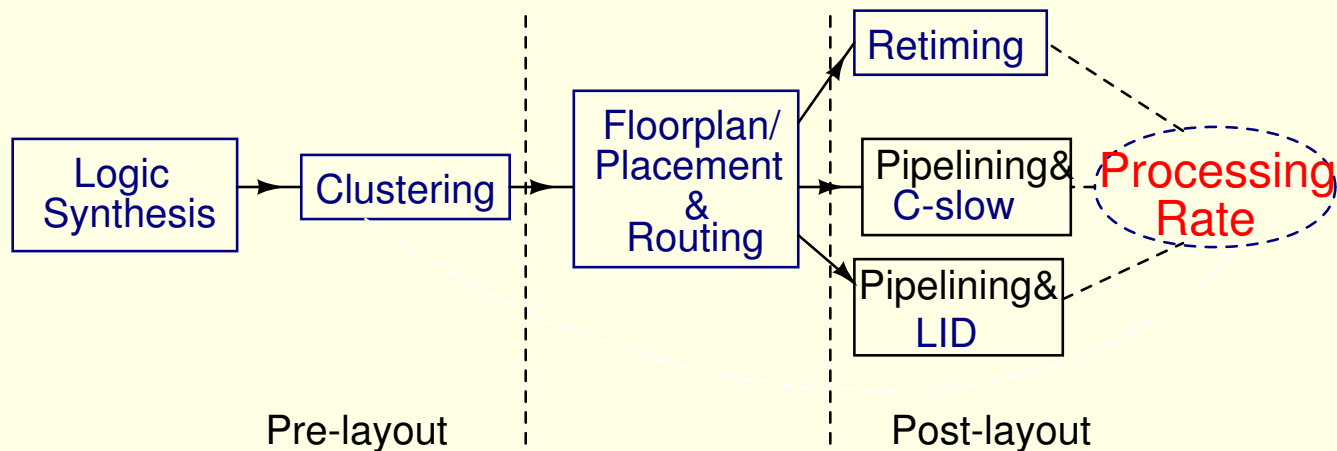
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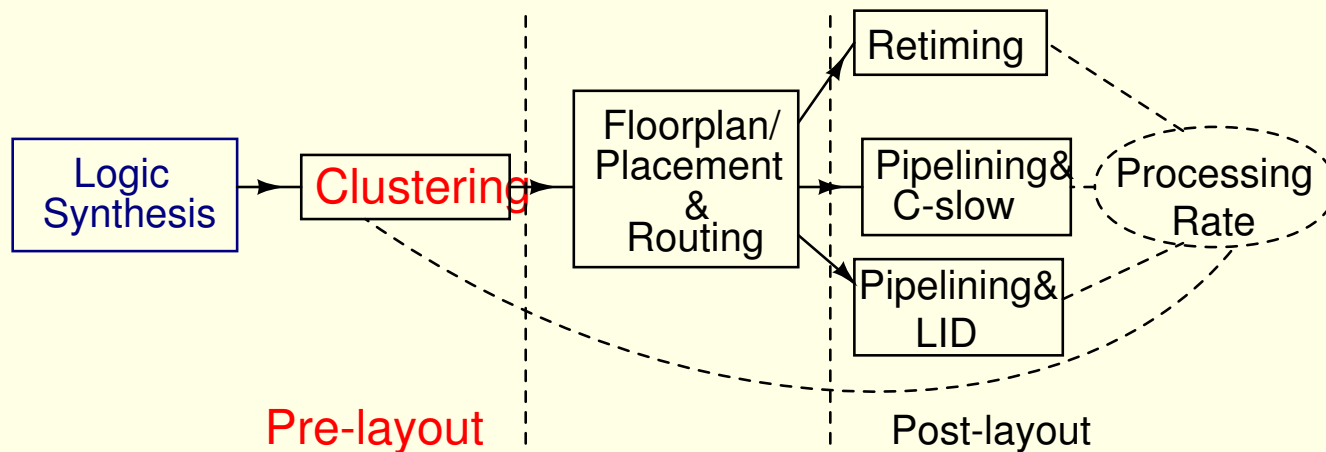
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- Our take
  - maximizing post-layout processing rate by **minimizing max-cycle-ratio** during pre-layout clustering

$$\text{max-cycle-ratio} \downarrow \Rightarrow \frac{1}{\text{max-cycle-ratio}} \uparrow \Rightarrow \text{max-processing-rate} \uparrow$$

## Assumptions

- How to evaluate global interconnect delay BEFORE layout?

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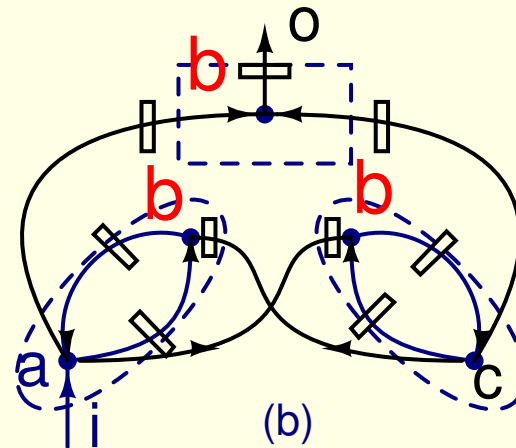
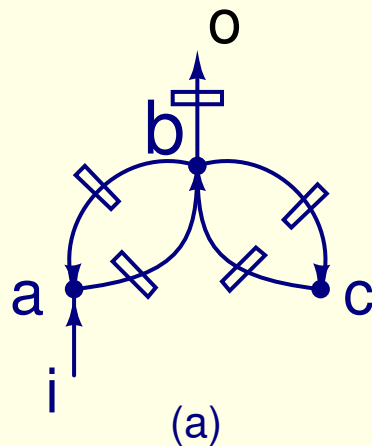
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Pan *et al.* [TCAD'98]

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- Ignore PI→PO paths
  - FFs can be pipelined from PI to reduce period

## Problem formulation

### [Optimal Clustering]

- Given
  - $G = (V, E)$  strongly connected,  $V$  gates,  $E$  interconnects
  - Each  $v \in V$  has a delay  $d(v)$  and a size  $s(v)$ ;  
Each  $e \in E$  has a delay  $d(e)$ , a size  $s(e)$  and a weight  $w(e)$  (# of FFs).
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  - A clustering with possible gate replication
  - **Maximum-cycle-ratio is minimized**

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- Minimize period lower bound by binary search
- Labeling for feasibility checking by binary search
  - \* Need to start from PI
- Pre-computed all pair longest path matrix

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## Useful results from previous works

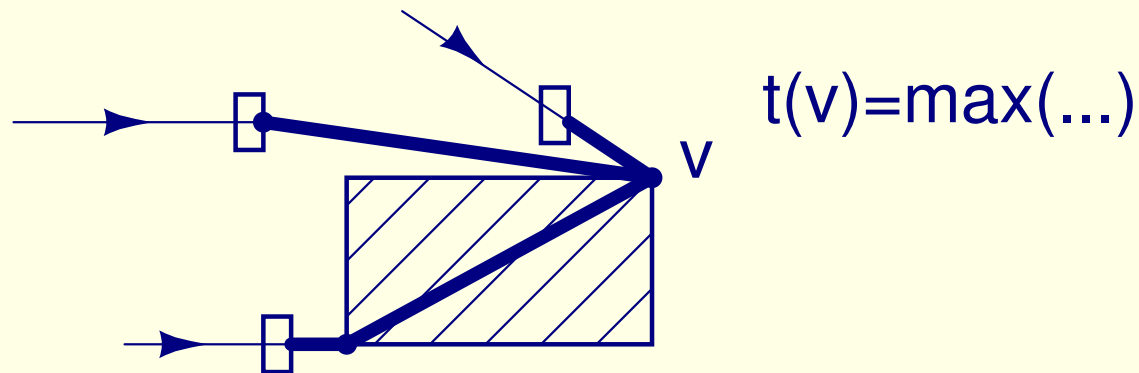
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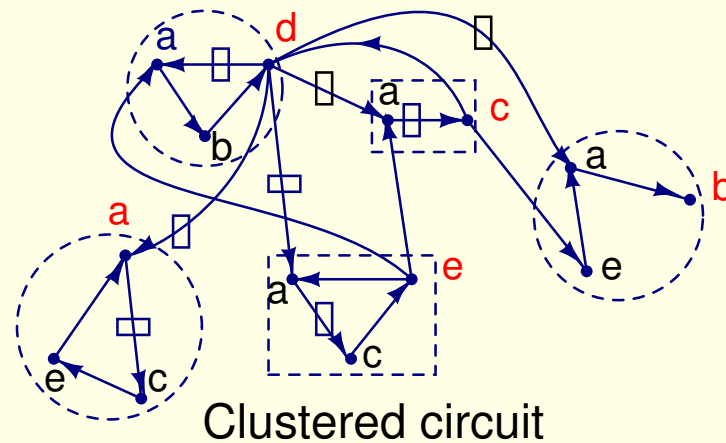
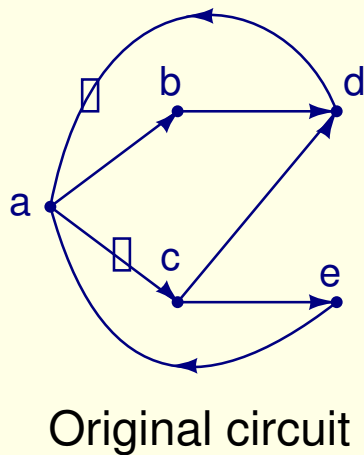
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[Optimal Clustering]

(Objective)

Minimize  $\phi$

(Timing constraints)

$$t(v) \geq 0, \quad \forall v \in V \quad (1)$$

$$t(v) \leq t(v'), \quad \forall \text{ replicate } v' \text{ of } v \quad (2)$$

$$t(v) \geq t(u) + d(u, v) - w(u, v)\phi, \quad \forall \text{ local } (u, v) \in E_c \quad (3)$$

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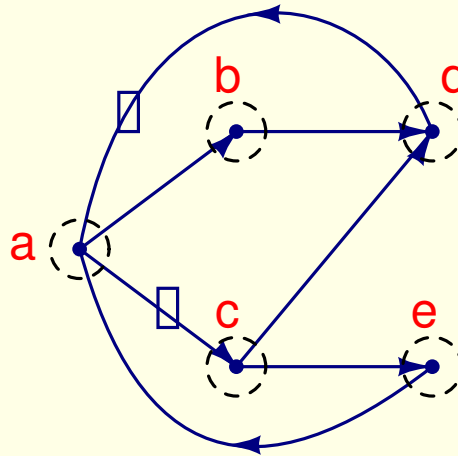
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    - \* If yes, new clustering has a smaller  $\phi$
    - \* If no, current  $\phi$  is optimal

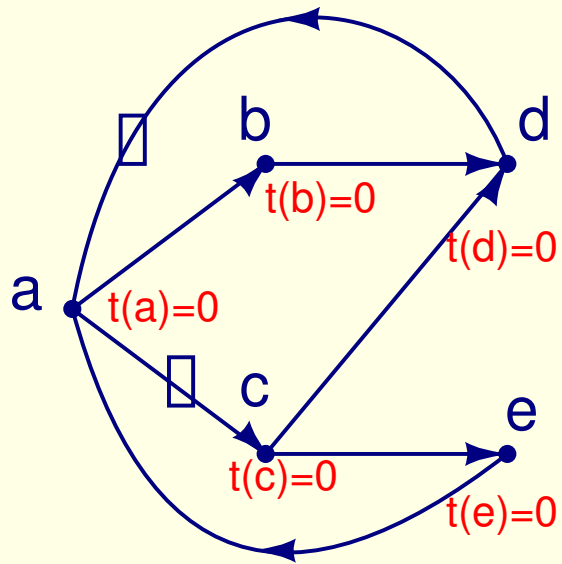
## Constructive clustering under $\phi$

- Notations

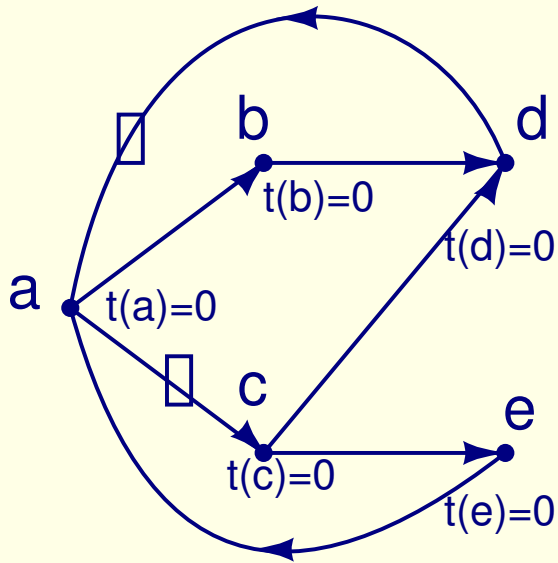
- Arrival time vector  $\mathsf{T} = (t(1), t(2), \dots, t(|V|))$
- Bottom element ( $\perp$ ):  $t(v) = 0, \forall v \in V$

# Constructive clustering under $\phi$

1. Start with  $\mathbb{T} = \perp$



## Constructive clustering under $\phi$



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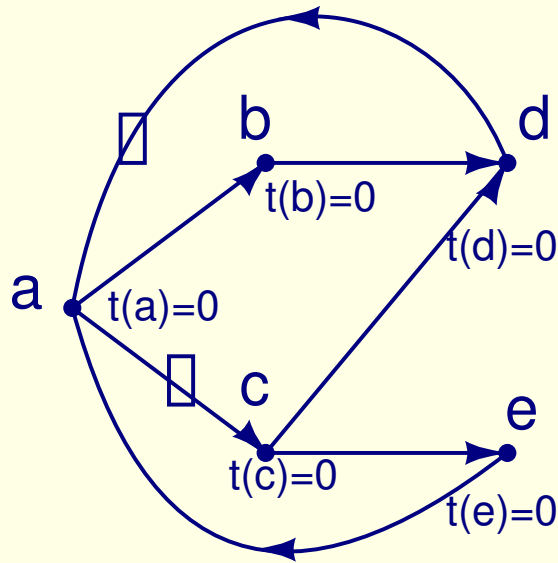
2. Satisfy (1) and (5) by Bellman-Ford's algorithm

• Denoted as  $BF(T, \phi)$

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3. **Satisfy (2), (3), (4) and (6)**

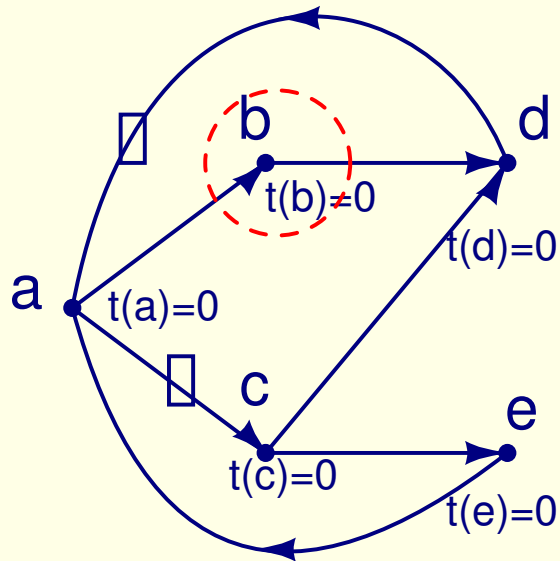
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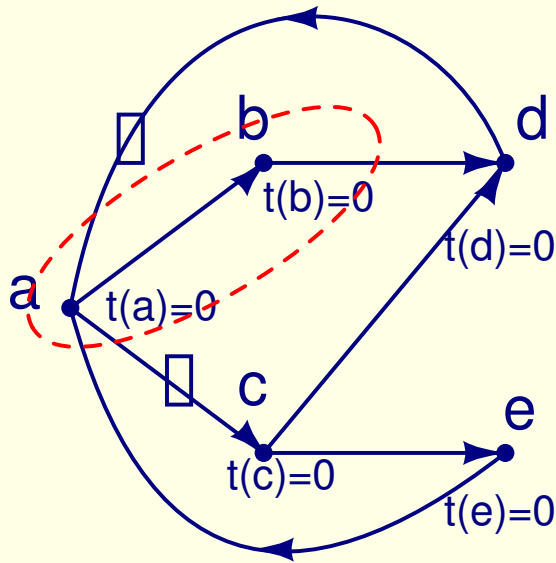
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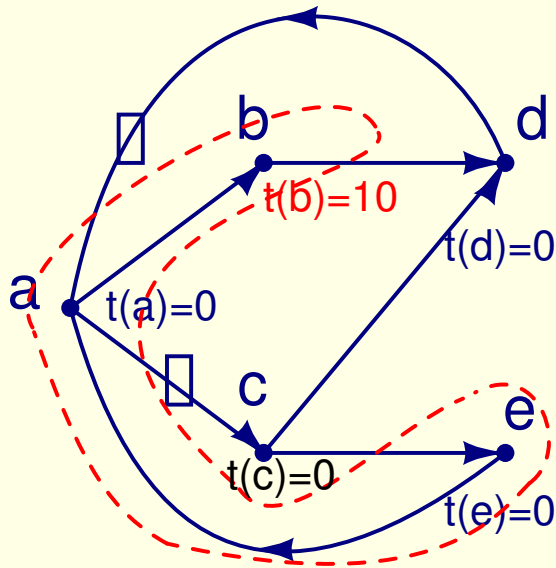
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  - **Include one critical input at a time**
    - path  $p : u \rightsquigarrow v$  is in  $c_v$
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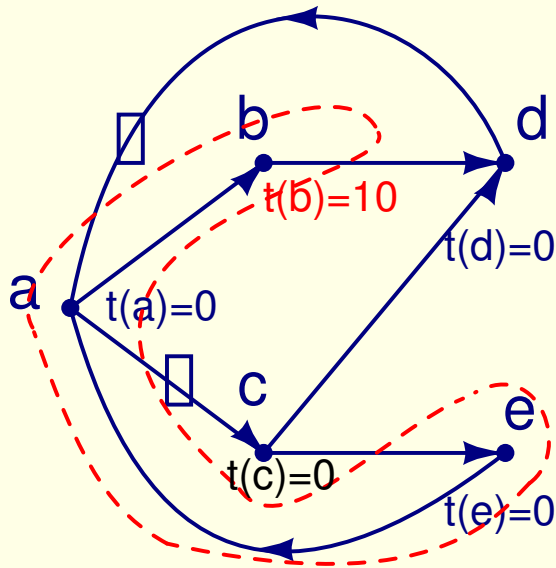
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## Constructive clustering under $\phi$



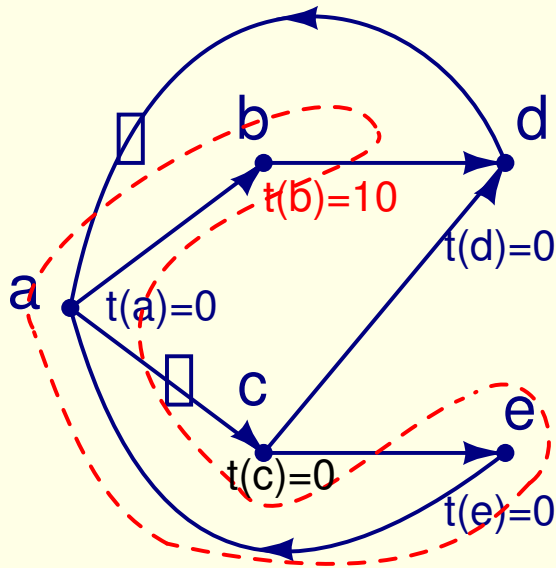
1. Start with  $T = \perp$
2. Satisfy (1) and (5) by Bellman-Ford's algorithm
  - Denoted as  $BF(T, \phi)$
3. **Construct  $c_v$  rooted at  $v, \forall v \in V$** 
  - **Include one critical input at a time**
    - path  $p : u \rightsquigarrow v$  is in  $c_v$
    - $t(v) = t(u) + D + d(p) - w(p)\phi$

## Constructive clustering under $\phi$



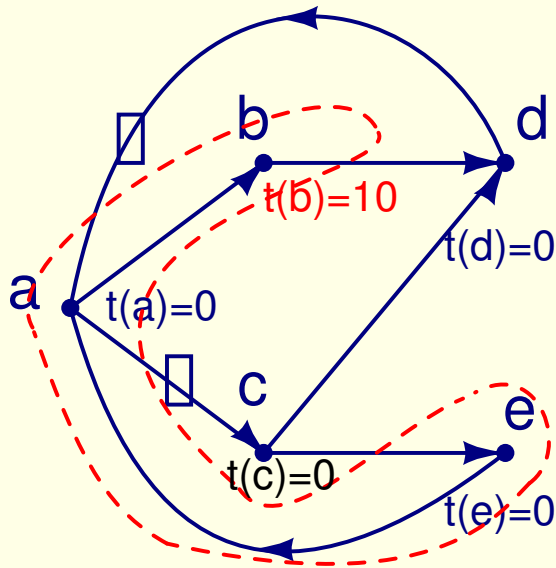
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3. **Construct  $c_v$  rooted at  $v, \forall v \in V$** 
  - **Include one critical input at a time**
  - **Size  $|c_v| = A$ , or no critical input**

## Constructive clustering under $\phi$



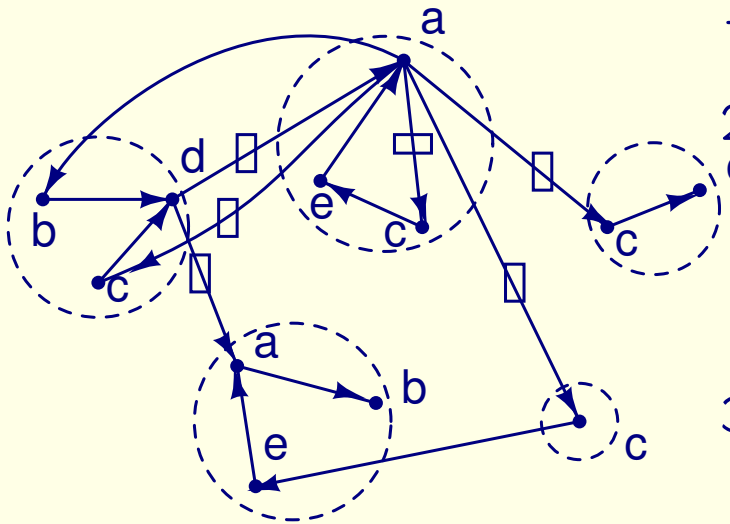
1. Start with  $T = \perp$
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  - Denoted as  $BF(T, \phi)$
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  - Denoted as  $\mathcal{L}_v(T, \phi)$

## Constructive clustering under $\phi$



1. Start with  $T = \perp$
2. Satisfy (1) and (5) by Bellman-Ford's algorithm
  - Denoted as  $BF(T, \phi)$
3. **Construct  $c_v$  rooted at  $v, \forall v \in V$** 
  - Denoted as  $\mathcal{L}_v(T, \phi)$
  - (2), (3), (4) and (6) are satisfied at  $c_v$
  - **BUT (1) and (5) may be violated**

## Constructive clustering under $\phi$



1. Start with  $\mathbb{T} = \perp$
2. Satisfy (1) and (5) by Bellman-Ford's algorithm

- Denoted as  $BF(\mathbb{T}, \phi)$

3. Construct  $c_v$  rooted at  $v$ ,  $\forall v \in V$

- Denoted as  $\mathcal{L}_v(\mathbb{T}, \phi)$

4. **Iterate 2 and 3 until all satisfied**

- **Denote one iteration as**

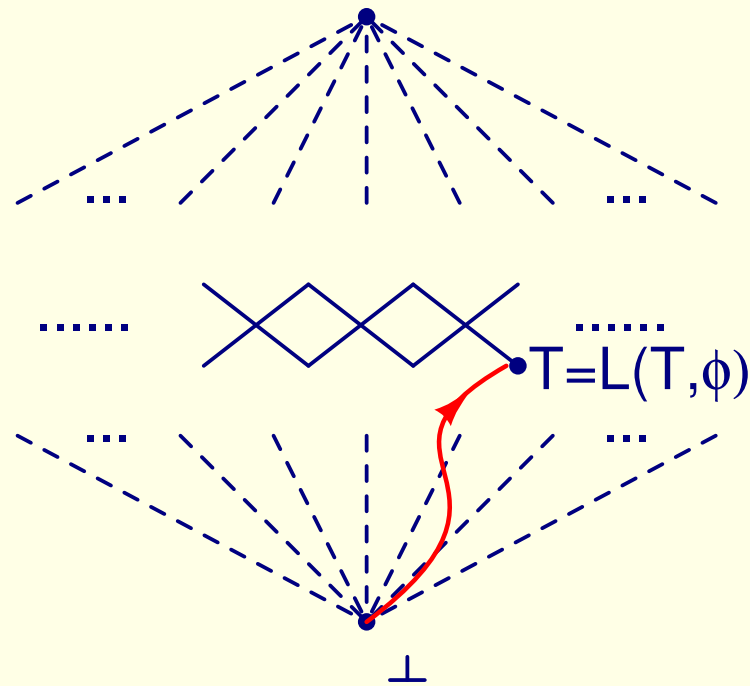
$$\mathcal{L}(\mathbb{T}, \phi) \triangleq BF\left(\left(\mathcal{L}_1(\mathbb{T}, \phi), \dots, \mathcal{L}_{|V|}(\mathbb{T}, \phi)\right), \phi\right)$$

## Constructive clustering under $\phi$

- All constraints are satisfied  $\iff T = \mathcal{L}(T, \phi)$ 
  - $T$  is called a **fixpoint** of  $\mathcal{L}$  under  $\phi$

## Constructive clustering under $\phi$

- $\phi$  is feasible  $\iff \mathcal{L}$  has a fixpoint under  $\phi \iff$  Applying  $\mathcal{L}$  from  $\perp$  is finitely convergent to a fixpoint



## Constructive clustering under $\phi$

- $\phi$  is feasible  $\iff \mathcal{L}$  has a fixpoint under  $\phi \iff$  Applying  $\mathcal{L}$  from  $\perp$  is finitely convergent to a fixpoint
- $\phi$  is infeasible  $\Leftarrow \top > BF(\perp, \phi)$

## Optimality checking

- $\phi$  is optimal  $\Leftrightarrow \phi - 1/(|V|N_{\text{ff}})^2$  is infeasible
  - $\phi$  is the maximum-cycle-ratio of a clustering satisfying size constraint
  - $N_{\text{ff}}$  is the max # of FF on any acyclic path in  $G$

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  - $\phi$  is the maximum-cycle-ratio of a clustering satisfying size constraint
  - $N_{\text{ff}}$  is the max # of FF on any acyclic path in  $G$
- Optimal  $\phi \geq$  maximum-cycle-ratio of  $G$

# Optimal Clustering Algorithm

```
 $c_v^{\text{opt}} \leftarrow c_v \leftarrow \{v\}, \forall v \in V;$   
 $\phi^{\text{opt}} \leftarrow \phi \leftarrow \text{maximum cycle ratio of } G_c;$   
 $\phi_{\text{lb}} \leftarrow \text{maximum cycle ratio of } G;$   
While  $(\phi \geq \phi_{\text{lb}} + 1/(|V|N_{\text{ff}})^2)$  do  
     $\phi \leftarrow \phi - 1/(|V|N_{\text{ff}})^2;$   
     $T \leftarrow BF(\perp, \phi);$   
    While  $\left( (T \neq \mathcal{L}(T, \phi)) \wedge (\neg(T > BF(\perp, \phi))) \right)$  do  
         $T \leftarrow \mathcal{L}(T, \phi);$   
    If  $(T > BF(\perp, \phi))$  then  
        break;  
    Update  $\phi, c^{\text{opt}}$  and  $\phi^{\text{opt}};$   
Return  $c^{\text{opt}}$  and  $\phi^{\text{opt}};$ 
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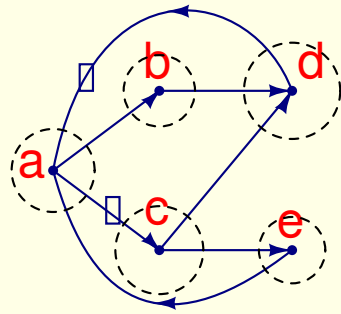
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While  $(\phi \geq \phi_{\text{lb}} + 1/(|V|N_{\text{ff}})^2)$  do  
     $\phi \leftarrow \phi - 1/(|V|N_{\text{ff}})^2;$  Resultant improvement is not small  
     $T \leftarrow BF(\perp, \phi);$   
    While  $\left( (T \neq \mathcal{L}(T, \phi)) \wedge (\neg(T > BF(\perp, \phi))) \right)$  do  
         $T \leftarrow \mathcal{L}(T, \phi);$   
    If  $(T > BF(\perp, \phi))$  then  
        break;  
    Update  $\phi, c^{\text{opt}}$  and  $\phi^{\text{opt}};$   
Return  $c^{\text{opt}}$  and  $\phi^{\text{opt}};$ 
```

# An example ( $A=3, D=10$ )



$\phi=30$



$\phi=20$

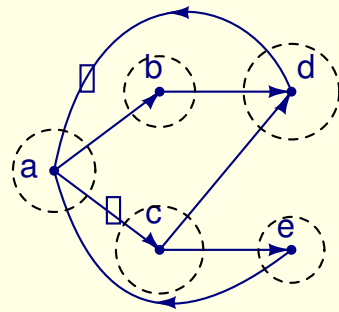


$\phi=10$

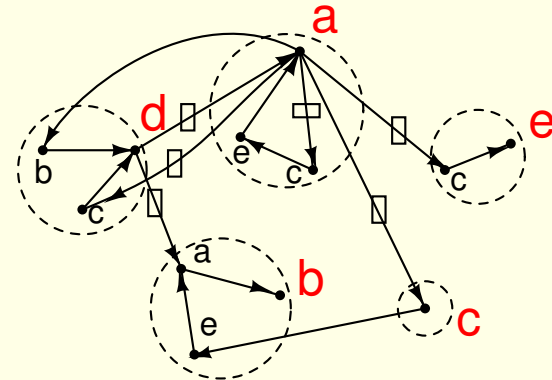


$\phi=15$

# An example ( $A=3, D=10$ )



$\phi=30$



$\phi=20$

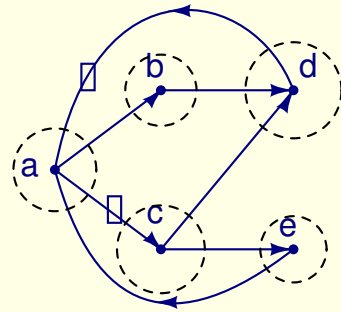


$\phi=10$

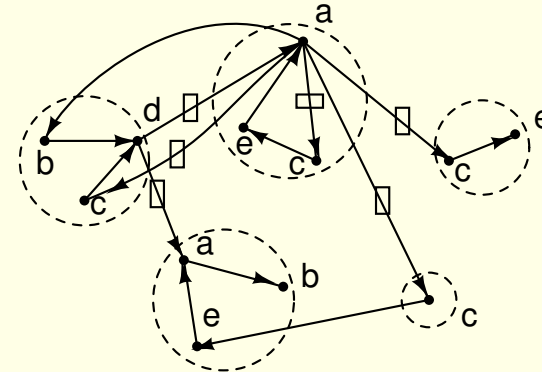


$\phi=15$

# An example ( $A=3, D=10$ )



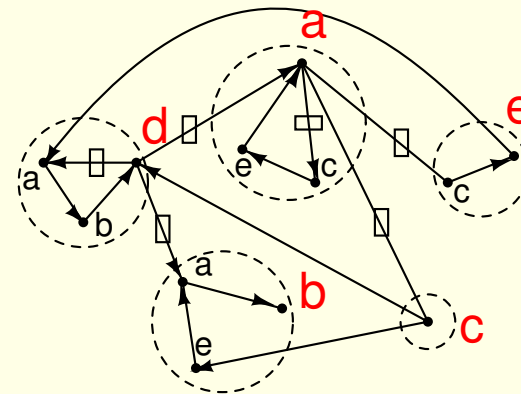
$\phi=30$



$\phi=20$

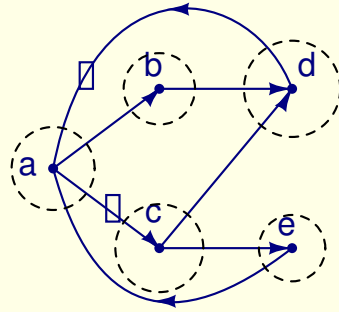


$\phi=10$

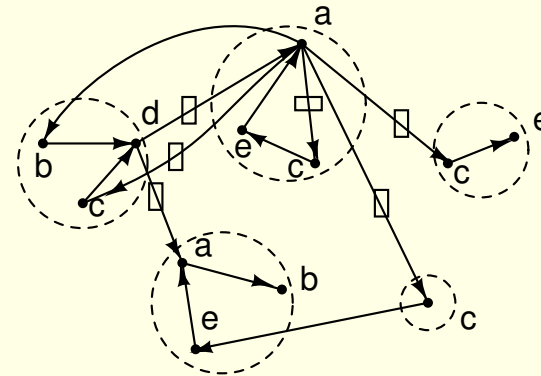


$\phi=15$

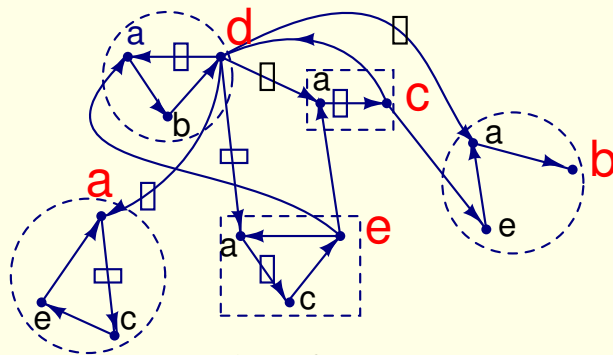
# An example ( $A=3, D=10$ )



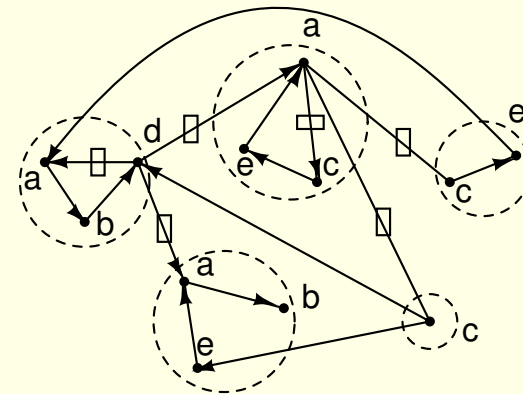
$\phi=30$



$\phi=20$



$\phi=10$



$\phi=15$

## Experiment setup

- ISCAS89 benchmark
- Connect  $PO \rightarrow FF \rightarrow PI$
- Gate size 1, gate delay 1
- Interconnect size 0, intra-cluster delay 0, inter-cluster  $D = 2$
- $A = 5\%|V|, 10\%|V|, 20\%|V|$

## Optimal Maximum-cycle-ratio

Circuit	$A = 5\% V $			$A = 10\% V $			$A = 20\% V $		
	$\phi^{\text{opt}}$	#iter	time(s)	$\phi^{\text{opt}}$	#iter	time(s)	$\phi^{\text{opt}}$	#iter	time(s)
s208	13.00	4	0.20	11.00	4	0.09	10.00	3	0.04
s349	18.00	14	1.73	16.00	21	0.47	14.67	14	1.39
s420	14.00	3	0.33	13.00	3	0.34	12.00	2	0.00
s635	75.00	4	5.71	70.00	4	8.02	68.00	4	6.43
s838	17.00	3	0.69	16.00	2	0.01	16.00	2	0.01
s1196	26.00	3	2.86	25.00	3	3.40	24.00	2	0.02
s1423	55.00	3	2.77	53.00	2	0.07	53.00	2	0.06
s1512	23.78	15	166.36	22.50	8	0.79	22.50	8	0.68
s3330	14.33	11	11.34	14.00	10	1.82	14.00	10	1.82
s4863	30.25	8	133.54	30.00	5	3.85	30.00	5	3.70
s5378	21.00	3	0.94	21.00	3	0.73	21.00	3	0.73
s9234	38.00	3	3.78	38.00	3	2.98	38.00	3	2.98
s35932	27.00	4	48.13	27.00	4	47.79	27.00	4	46.59
s38584	48.00	2	21.92	48.00	2	21.90	48.00	2	21.46

## Run-time comparison with Pan *et al.*'s work

Circuit	time/step (s)	
	Pan's	presented
s208	0.01	0.00
s349	0.19	0.02
s420	0.03	0.00
s635	0.10	0.03
s838	0.13	0.01
s1196	0.08	0.02
s1423	0.65	0.04
s1512	1.22	0.06
s3330	1.07	0.17
s4863	82.30	1.04
s5378	7.58	0.31
s9234	n/a	1.20
s35932	n/a	11.46
s38584	n/a	11.07
arith	19.39X	1
geo	12.78X	1

## Techniques for replication reduction

- We did not do experiments on area overhead
- Previous works [Sangiovanni-Vincentelli *et al.* ICCAD'91, Pan *et al.* TCAD'98, Cong *et al.* DAC'99]
  - Allow a cluster to have multiple outputs
  - Relax timing on non-critical paths
  - Merge small clusters

## Summary

- Processing rate is identified as an important metric for sequential circuits
- max processing rate  $\leq 1 /$  maximum-cycle-ratio (affected by clustering)
- A new algorithm is presented
  - Finds a clustering with minimal maximum-cycle-ratio
  - Efficient in practice
  - Incremental

# Erratum

- The algorithm presented in this talk is a little different from the one in the paper
  - There is a slip in the original proof of the termination criterion
  - The correct version can also be found in the technical report

**Thank you !**